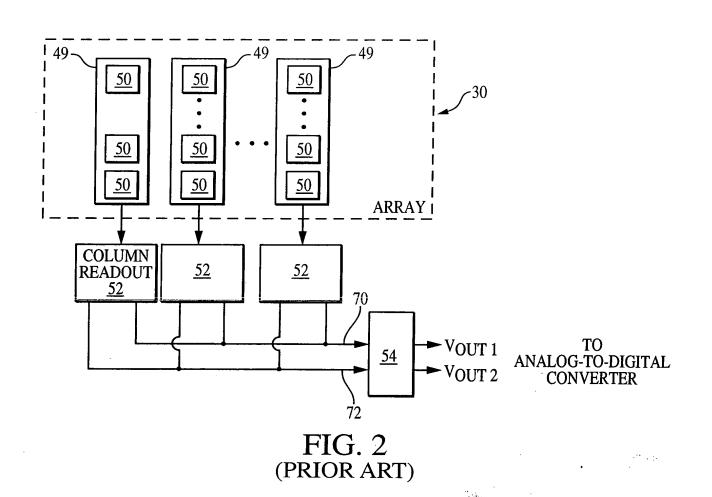


FIG. 1 (PRIOR ART)



Docket No.: M4065.0960/P960

Inventor: Alexander Krymski

Title: PIXEL SIGNAL BINNING AND INTERPOLATION IN

COLUMN CIRCUITS OF A SENSOR CIRCUIT

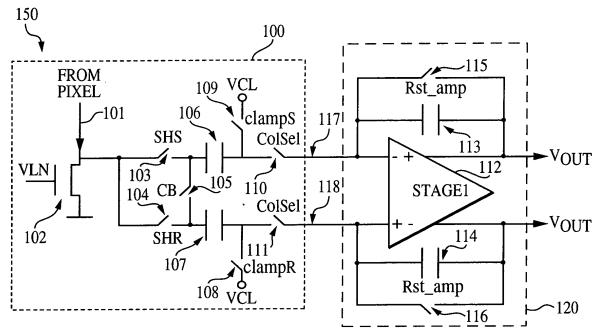


FIG. 3 (PRIOR ART)

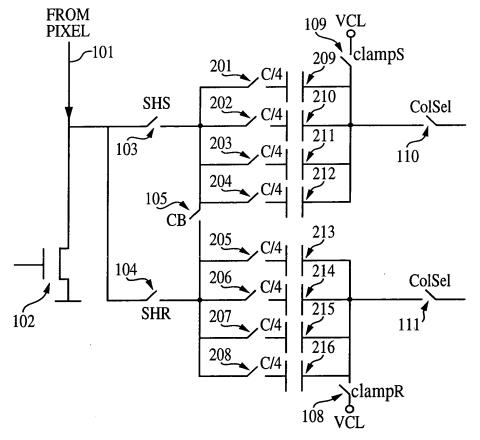
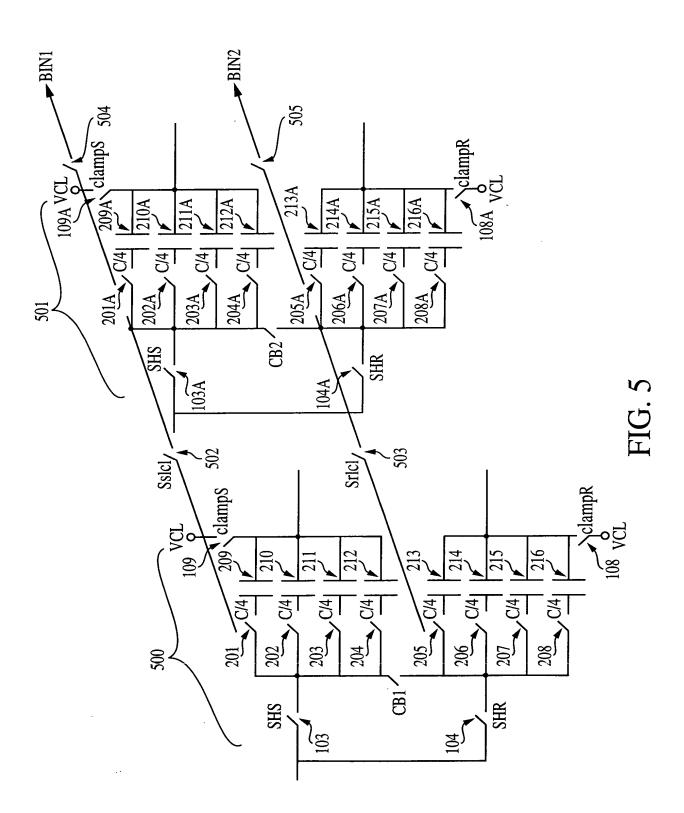


FIG. 4

Docket No.: M4065.0960/P960

Inventor: Alexander Krymski

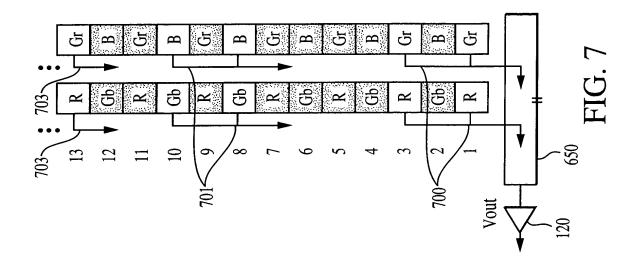
Title: PIXEL SIGNAL BINNING AND INTERPOLATION IN COLUMN CIRCUITS OF A SENSOR CIRCUIT

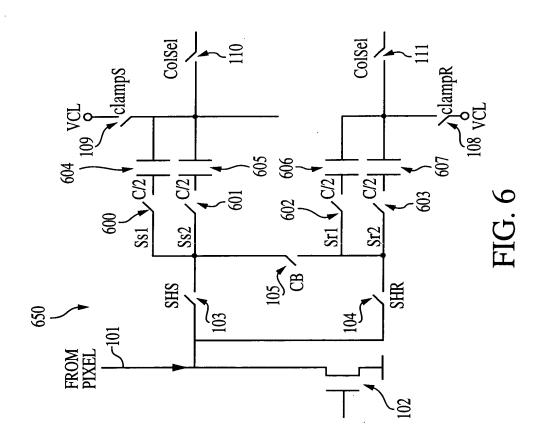


Docket No.: M4065.0960/P960

Inventor: Alexander Krymski

Title: PIXEL SIGNAL BINNING AND INTERPOLATION IN COLUMN CIRCUITS OF A SENSOR CIRCUIT REPLACEMENT SHEET

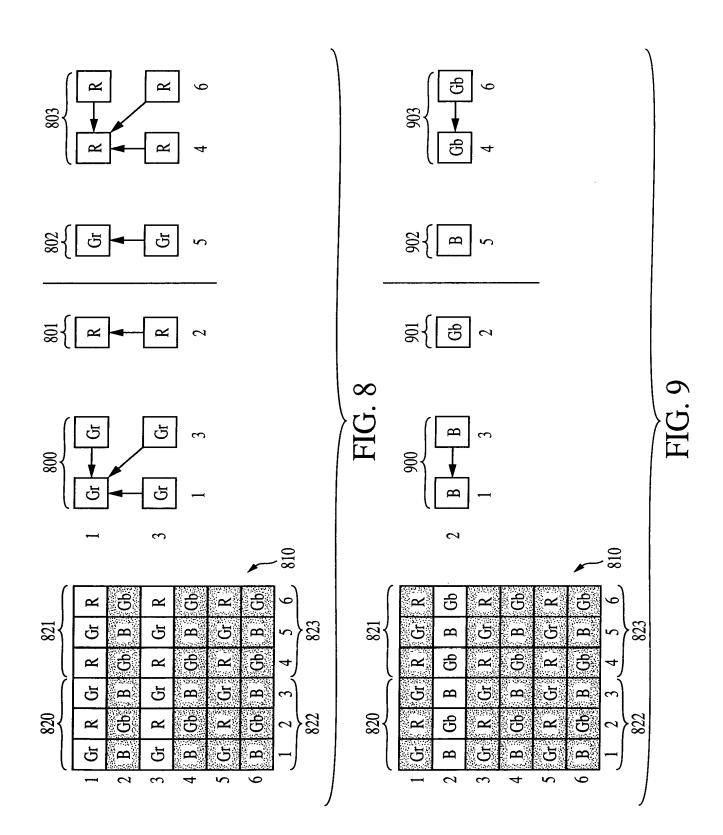




App No.: 10/705,843 Inventor: Alexander Krymski

Title: PIXEL SIGNAL BINNING AND INTERPOLATION IN

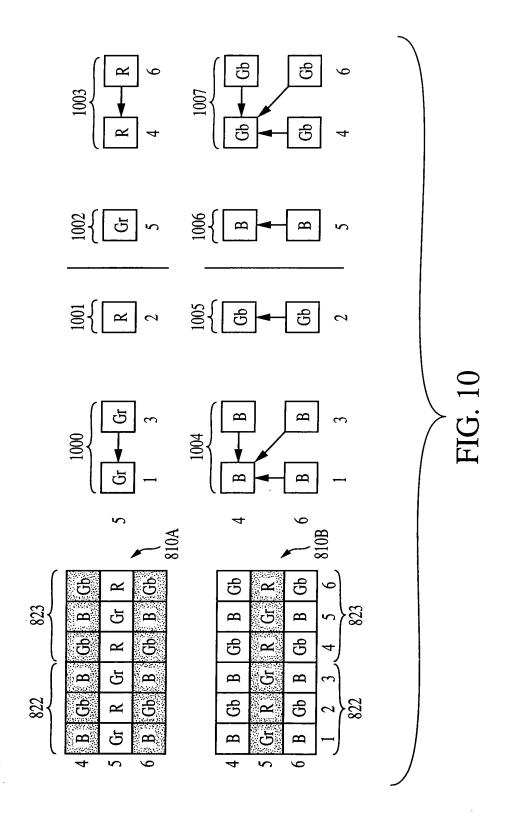
COLUMN CIRCUITS OF A SENSOR CIRCUIT



App No.: 10/705,843 Docket No.: M4065.0960/P960

Inventor: Alexander Krymski

Title: PIXEL SIGNAL BINNING AND INTERPOLATION IN COLUMN CIRCUITS OF A SENSOR CIRCUIT REPLACEMENT SHEET



Docket No.: M4065.0960/P960 App No.: 10/705,843

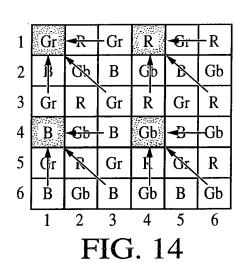
Inventor: Alexander Krymski

Title: PIXEL SIGNAL BINNING AND INTERPOLATION IN COLUMN CIRCUITS OF A SENSOR CIRCUIT

1	Gr	R	Gr	R	Gr	R	
2	В	Gb	В	Gb	В	Gb	
3	Gr	R	Gr	R	Gr	R	
4	В	Gb	В	Gb	В	Gb	
5	Gr	R	Gr	R	Gr	R	
6 B Gb B Gb B Gb							
,	1	2	3	4	5	6	
FIG. 11							

1	Gr	R	Gr	R	Gr	R
2	В	Gb	В	Gb	В	Gb
3	Gr	R	Gr	R	Gr	R
4	В	Gb	В	Gb	В	Gb
5	Gr	R	Gr	R	Gr	R
6	В	Gb	В	Gb	В	Gb
	1	2	3	4	5	6
FIG. 12						

1	Gr	R	Gr	R	Gr	R	
2	В	Gb	В	Gb	В	Gb	
3	Gr	R	Gr	R	Gr	R	
4	В	Gb	В	Gb	В	Gb	
5	Gr	R	Gr	R	Gr	R	
6	В	Gb	В	Gb	В	Gb	
•	1	2	3	4	5	6	
	FIG 13						

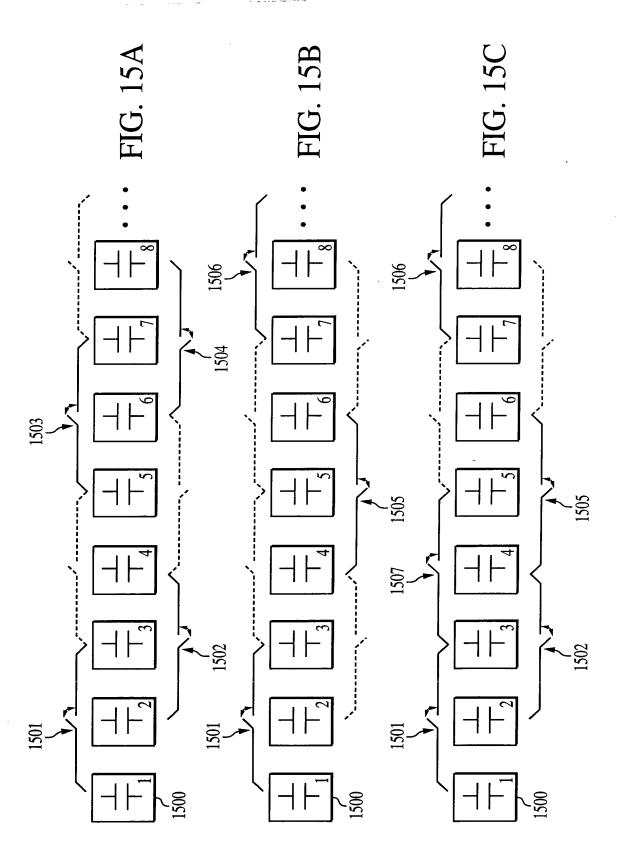


Docket No.: M4065.0960/P960

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Title: PIXEL SIGNAL BINNING AND INTERPOLATION IN

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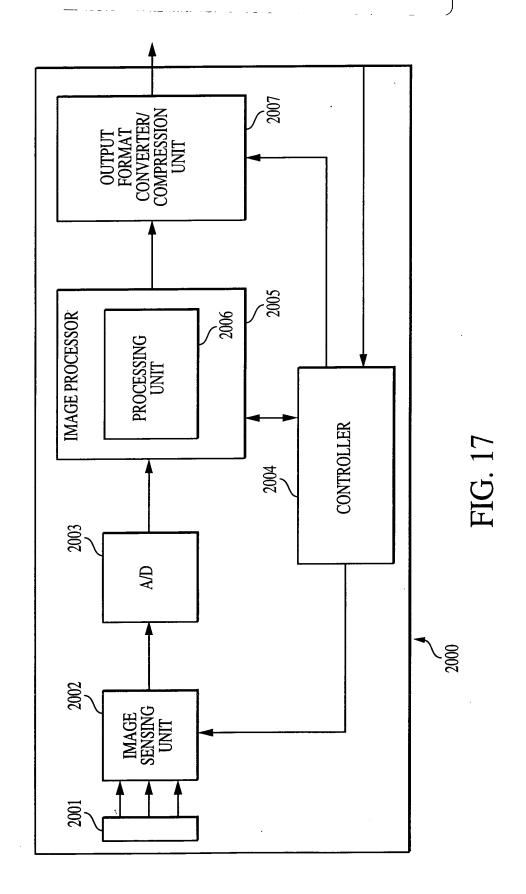
REPLACEMENT SHEET

Су	Ye	Су	Ye	1600
G	Mg	G	Mg	
Су	Ye	Су	Ye	
Mg	G	Mg	G	

FIG. 16

Docket No.: M4065.0960/P960

Inventor: Alexander Krymski
Title: PIXEL SIGNAL BINNING AND INTERPOLATION IN
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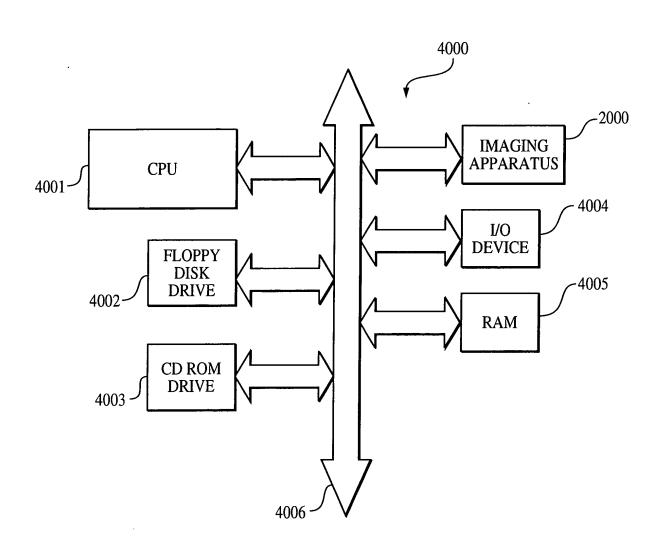


FIG. 18